



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#6/3  
700  
4/6-03

In re patent application of

William P. MOORE, et al.

Serial No.: 09/805,137

Group Art Unit: 2825

Filing Date: March 14, 2001

Examiner: Thompson, Annette M.

For: MICROPROCESSOR INCLUDING CONTROLLER FOR REDUCED POWER  
CONSUMPTION AND METHOD THEREFOR

Assistant Commissioner of Patents  
Washington, D.C. 20231

**AMENDMENT AND RESPONSE TO FIRST OFFICE ACTION  
AND RESTRICTION REQUIREMENT**

Sir:

In response to the Office Action dated February 26, 2003, please amend the application as follows:

**IN THE CLAIMS:**

**Please cancel claims 10-14 without prejudice or disclaimer.**

**REMARKS**

In response to the Examiner's restriction requirement, Applicant hereby elects the invention of Group I (e.g., drawn to a method of updating a semiconductor chip as defined by claims 5, 7-8, and 21), without traverse. Applicant reserves the opportunity to file a Divisional Application for the non-elected invention later.

Early, favorable prosecution on the merits is respectfully requested.

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